

Electronic Acknowledgement Receipt

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Application Number:	09943512
International Application Number:	
Confirmation Number:	8183
Title of Invention:	Multilayer wiring board and semiconductor device
First Named Inventor/Applicant Name:	Yasuo Osone
Customer Number:	20457
Filer:	Gregory Edwards Montone/David Snyder
Filer Authorized By:	Gregory Edwards Montone
Attorney Docket Number:	500.40530X00
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Request for Continued Examination (RCE)	RCE.pdf	188735 37497001e45a7b6dc85802c320c982d2392793c	no	1
Warnings:					
This is not a USPTO supplied RCE SB30 form.					
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2	Amendment Submitted/Entered with Filing of CPA/RCE	AMD114.pdf	1402054 c8c7b108b80a20a270e17c760889f5a839b230f7	no	16
Warnings:					
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3	Extension of Time	EOT.pdf	75850 a44986a3eccc70538f52f661ad13e0485a59bf2	no	1
Warnings:					
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4	Fee Worksheet (PTO-875)	fee-info.pdf	33523 4ff8db4c9f4264562f8188f1b46940c0b4d9f0c5	no	2
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Total Files Size (in bytes):			1700162		
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